

- ☐ Drafts
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  - S2: (1) ('5511013'.PN.
  - S1: (76) map near9 disable\$4 near9 address
  - S3: (119) (map list) near9 disable\$4 near9 address
- ☐ Favorites
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Search:     

DBs: US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM\_TDB

☒ PluralsDefault operator: ☐ Highlight all hit terms initially    

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Im
1	<input type="checkbox"/>	US 5511013 A	19960423	9	Low power consumption type one-chip microcomputer	713/321	713/322		Tokieda; Yusuke et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	US 6817015 B2	20041109	17	Microcontroller with modifiable program	717/168	711/103; 711/133;		Takata; Toshio	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	US 5933614 A	19990803	28	Isolation of PCI and EISA masters by masking control and	710/306			Tavallaei; Siamak et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

  

Ready

NUM